



10/822456

Cofe

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: MS Post Issue, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: May 10, 2005

Signature: \_\_\_\_\_

(Michael R. Hall)

Docket No.: 29925/39913  
(PATENT)**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Letters Patent of:

**Da-Soon LEE**

Patent No.: 6,846,713

Issued: January 25, 2005

For: METHOD FOR MANUFACTURING A  
NONVOLATILE MEMORY TRANSISTOR**REQUEST FOR CERTIFICATE OF CORRECTION  
PURSUANT TO 37 CFR 1.322**MS Post Issue  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**Certificate  
MAY 18 2005  
of Correction**

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted five typographical errors which should be corrected.

APPLICATION PAGE NO.	APPLICATION LINE NO.	COLUMN NO.	LINE NO.	ERROR
Specification Page 7	Abstract Line 3	Title Page Column Two	Line 4	PTO
Specification Page 7	Abstract Line 7	Title Page Column Two	Line 11	PTO
Specification Page 7	Abstract Line 8	Title Page Column Two	Line 13	PTO
Examiner's Amendment of September 24, 2004	Claim 1 Line 8	Column 4 Claim 1	Line 62	PTO
Examiner's Amendment of September 24, 2004	Claim 1 Line 11	Column 4 Claim 1	Line 67	PTO

The errors were not in the application as filed by applicant; accordingly no fee is required.

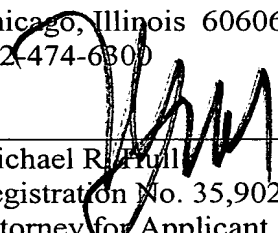
MAY 23 2005

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Respectfully submitted,

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By: \_\_\_\_\_

  
Michael R. Full  
Registration No. 35,902  
Attorney for Applicant

May 10, 2005

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 6,846,713  
APPLICATION NO. : 10/827,456  
ISSUE DATE : January 25, 2005  
INVENTOR(S) : Da-Soon Lee

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**Title Page**

**Field (57) Abstract, Line 4**

After "methods comprises the" please delete "steps of forming" and insert -- steps of: forming -- in its place.

**Title Page**

**Field (57) Abstract, Line 11**

After "of the select" please delete "transistor, after" and insert -- transistor; after -- in its place.

**Title Page**

**Field (57) Abstract, Line 13**

After "poly-1" please delete "layer, forming" and insert -- layer; forming -- in its place.

**Column 4**

**Claim 1, Line 62**

After "select" please delete "transistor," and insert -- transistor; -- in its place.

**Column 4**

**Claim 1, Line 67**

After "of a cell" please delete "transistor," and insert -- transistor; -- in its place.

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Dated: May 10, 2005

Signature:  (Michael R. Hull)

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MAY 23 2005